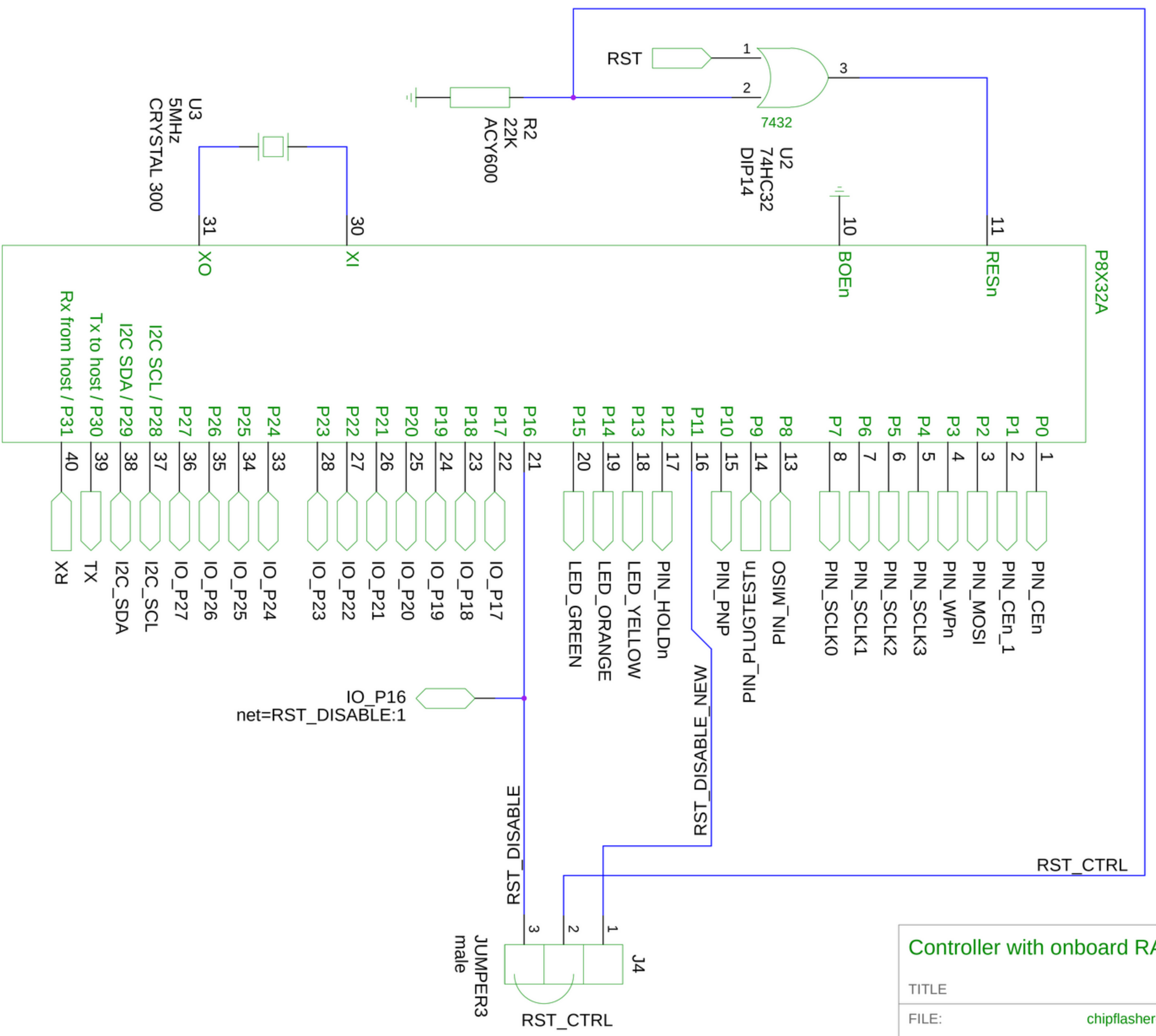


U1  
DIP40  
net=Vcc:12,32  
net=GND:9,29

This chip is precious, use socket.  
If Vdd falls below +2.7V, all pins become tristate.  
In order to decouple Vdd, use two 100nF caps, one left, one right.  
RESn is pulled up through 5K when BOEn is low.



### Controller with onboard RAM

TITLE	
FILE:	chipflasher-page01.sch
PAGE	OF

### Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

REVISION:	v0.6.9-0-77f27802 (master)
DRAWN BY:	kmx